

FIG. 1

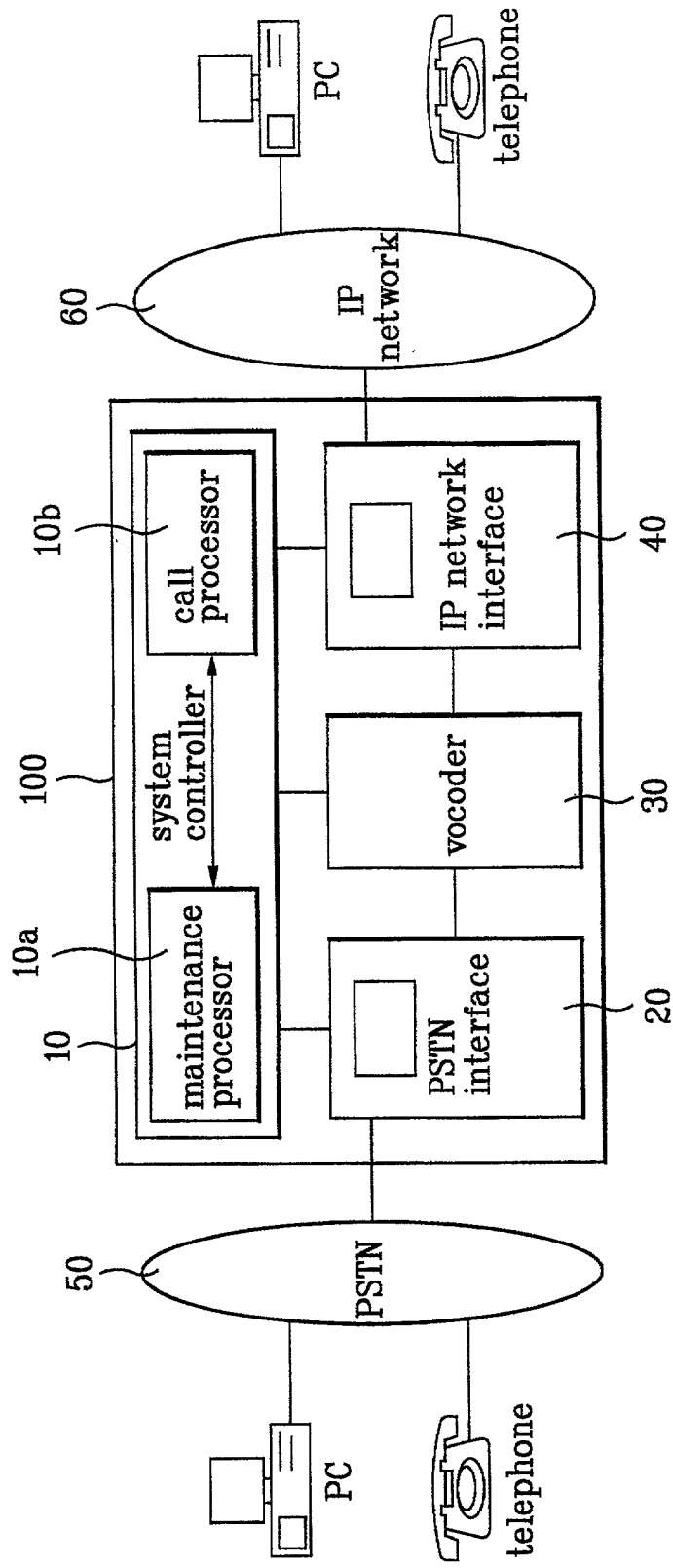


FIG. 2

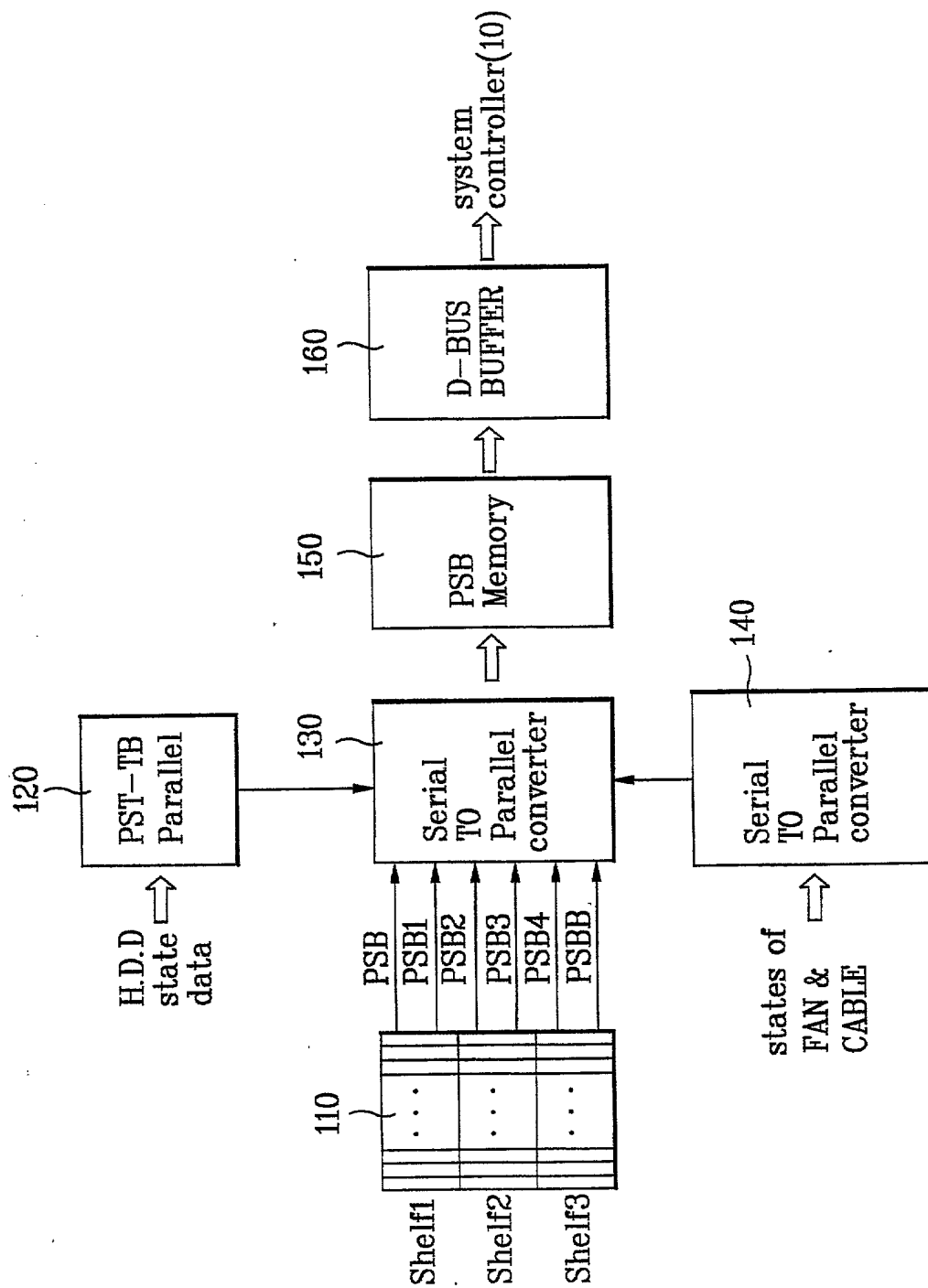


FIG. 3A

		Add	b7~b4	b3~b0	Remarks
shelf		0000	DPM1 LC2	DPM1 LC1	DKU,PSU : PSB_TB conversion from PSB-TB to PSB
		0001			
		0002	LC4	LC3	
		0003			
		0004	LC6	LC5	
		0005			
		0006	LC8	LC7	
		0007	RDCC1	RDCC0 (HUBC)	
shelf			DKU3,4 PSU0,1	DKU1,2 DKU5,6	
		0008	DPM2 LC2	DPM2 LC1	
		0009			
		000a	LC4	LC3	
		000b			
		000c	LC6	LC5	
		000d			
		000e	LC8	LC7	
		000f	RDCC1	RDCC0 (HUBC)	
			DKU3,4 PSU0,1	DKU1,2 DKU5,6	
		0010	DPM3 LC2	DPM3 LC1	
		0011			
		0012	LC4	LC3	
		0013			
		0014	LC6	LC5	
		0015			
		0016	LC8	LC7	
		0017	RDCC1	RDCC0 (HUBC)	
			DKU3,4 PSU0,1	DKU1,2 DKU5,6	

FIG. 3A

FIG. 4

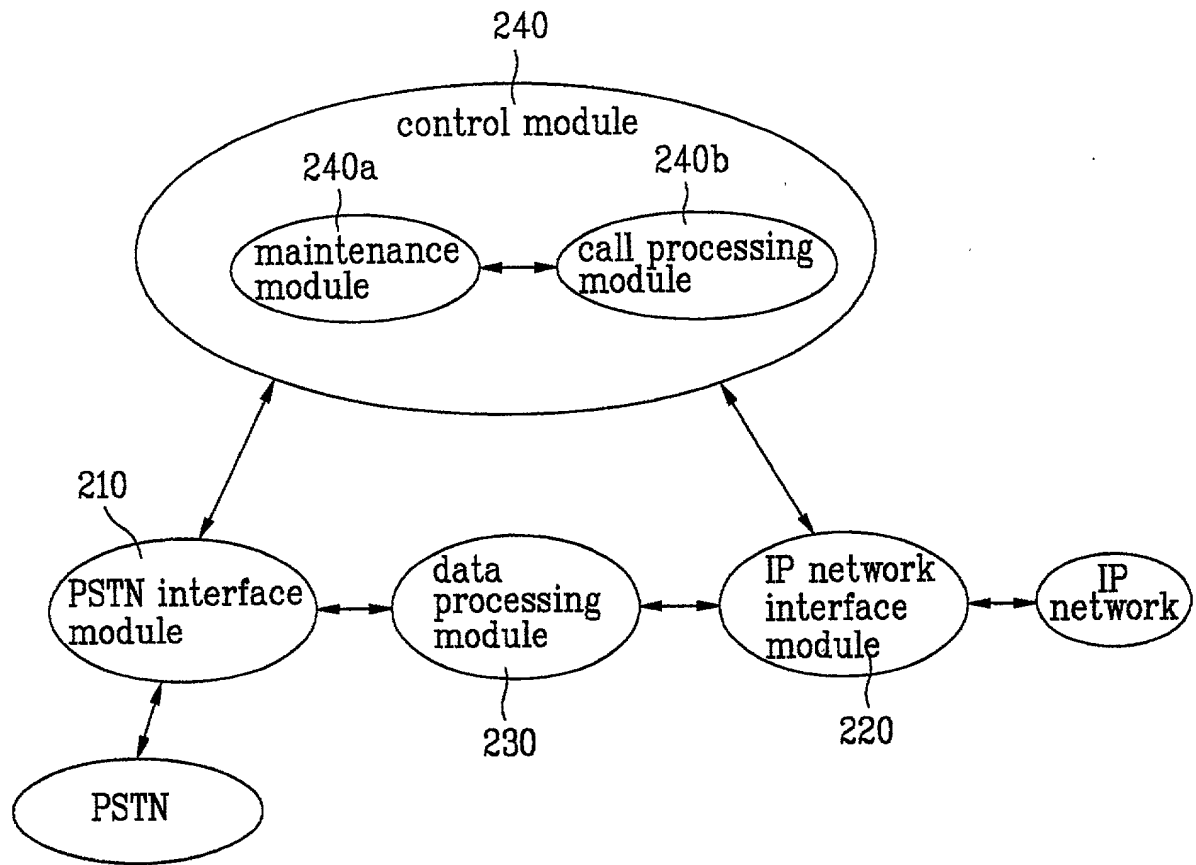
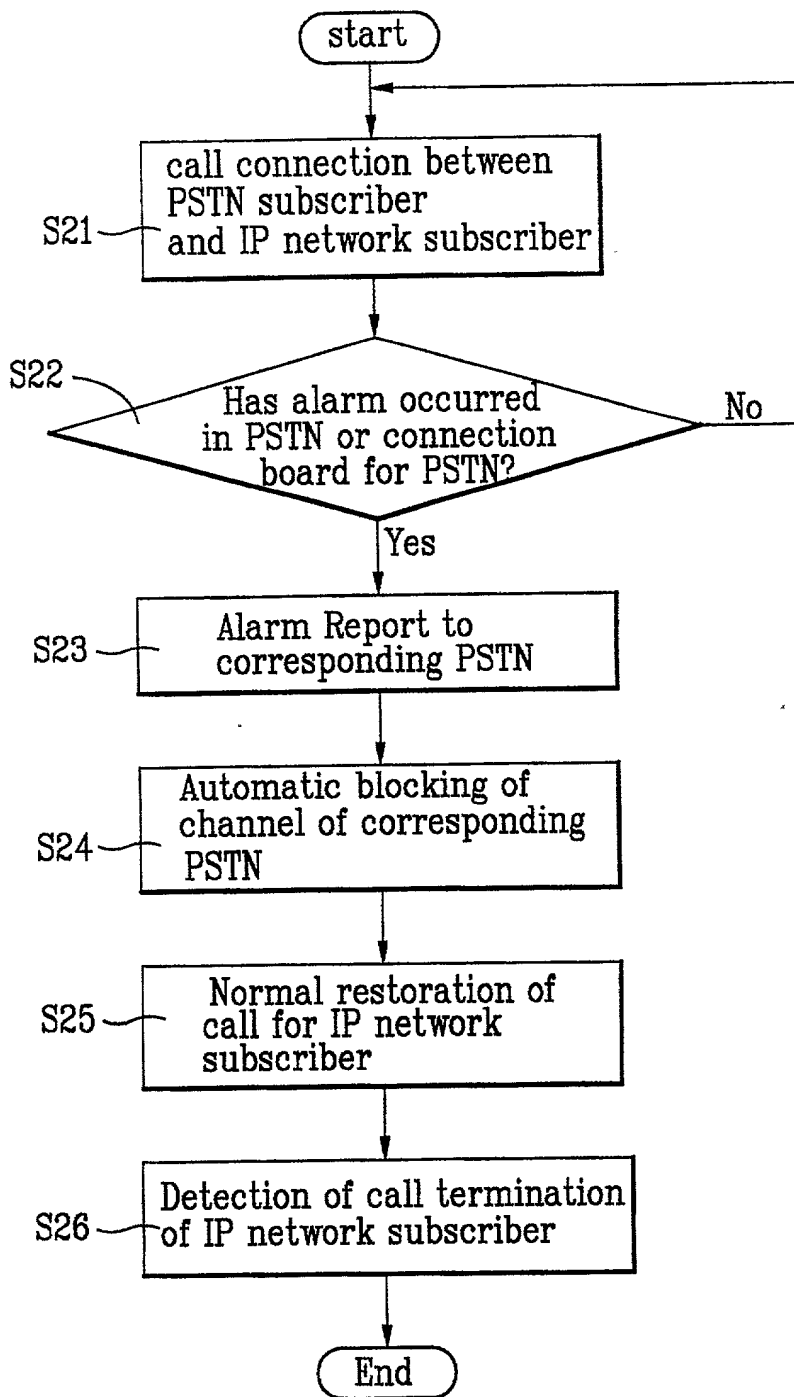
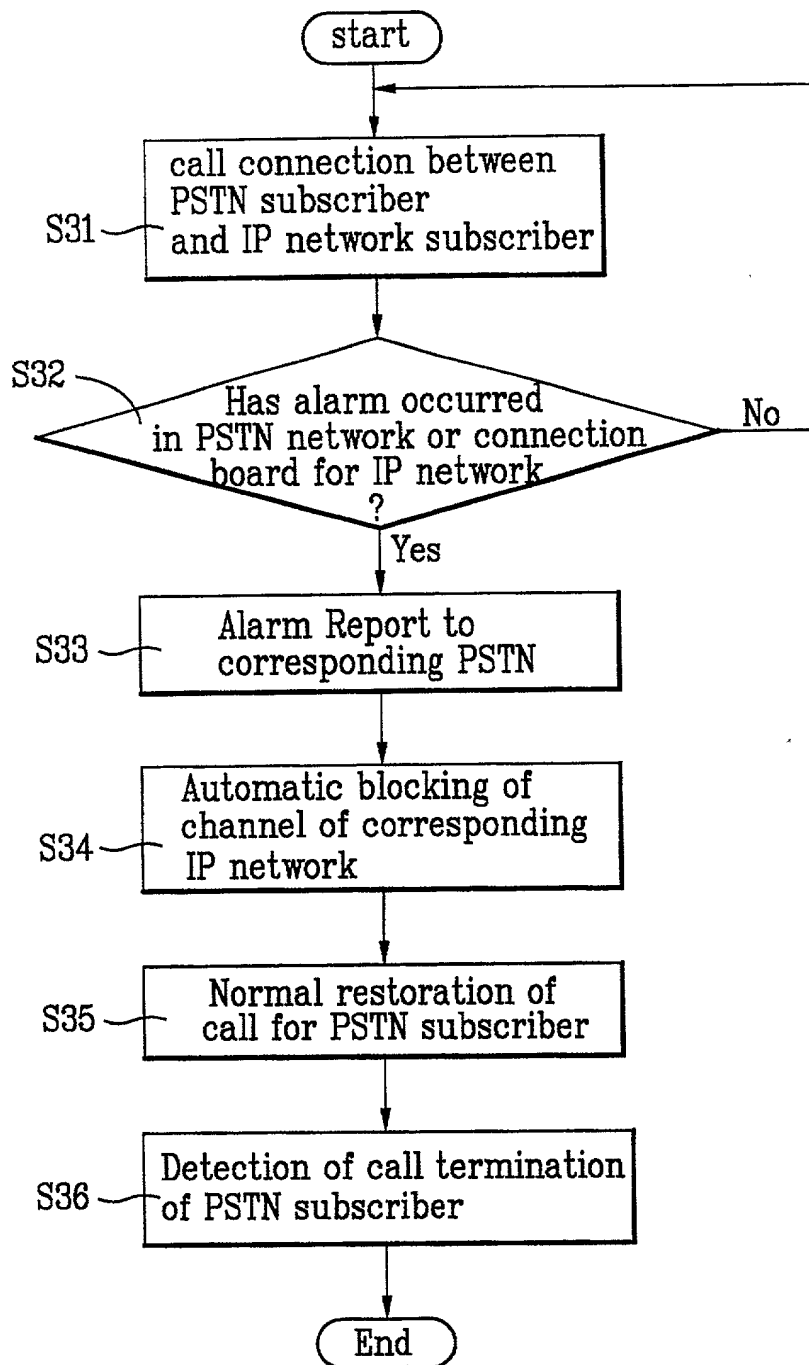


FIG. 5



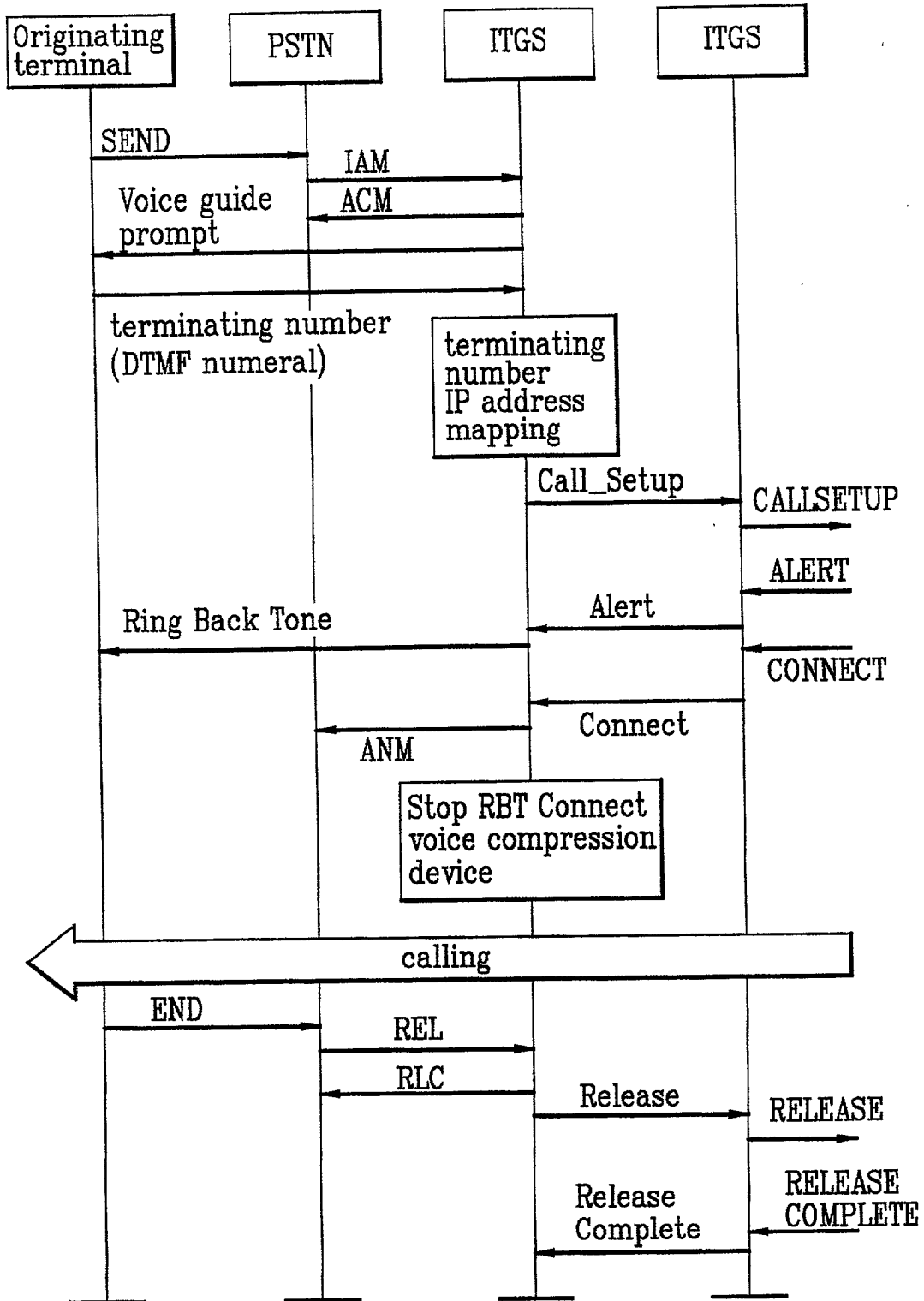
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FIG. 6



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FIG. 7



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FIG. 8

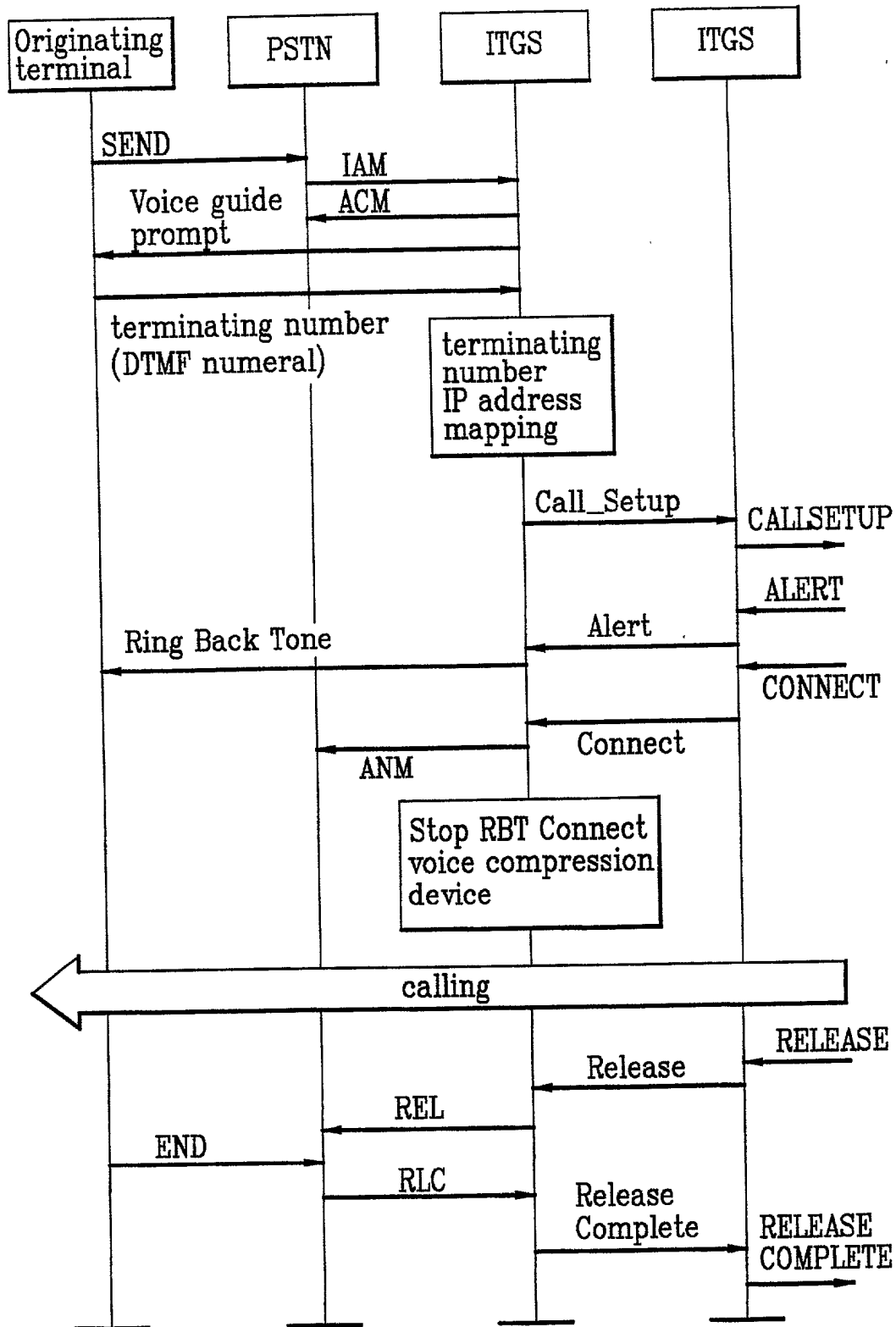


FIG. 9

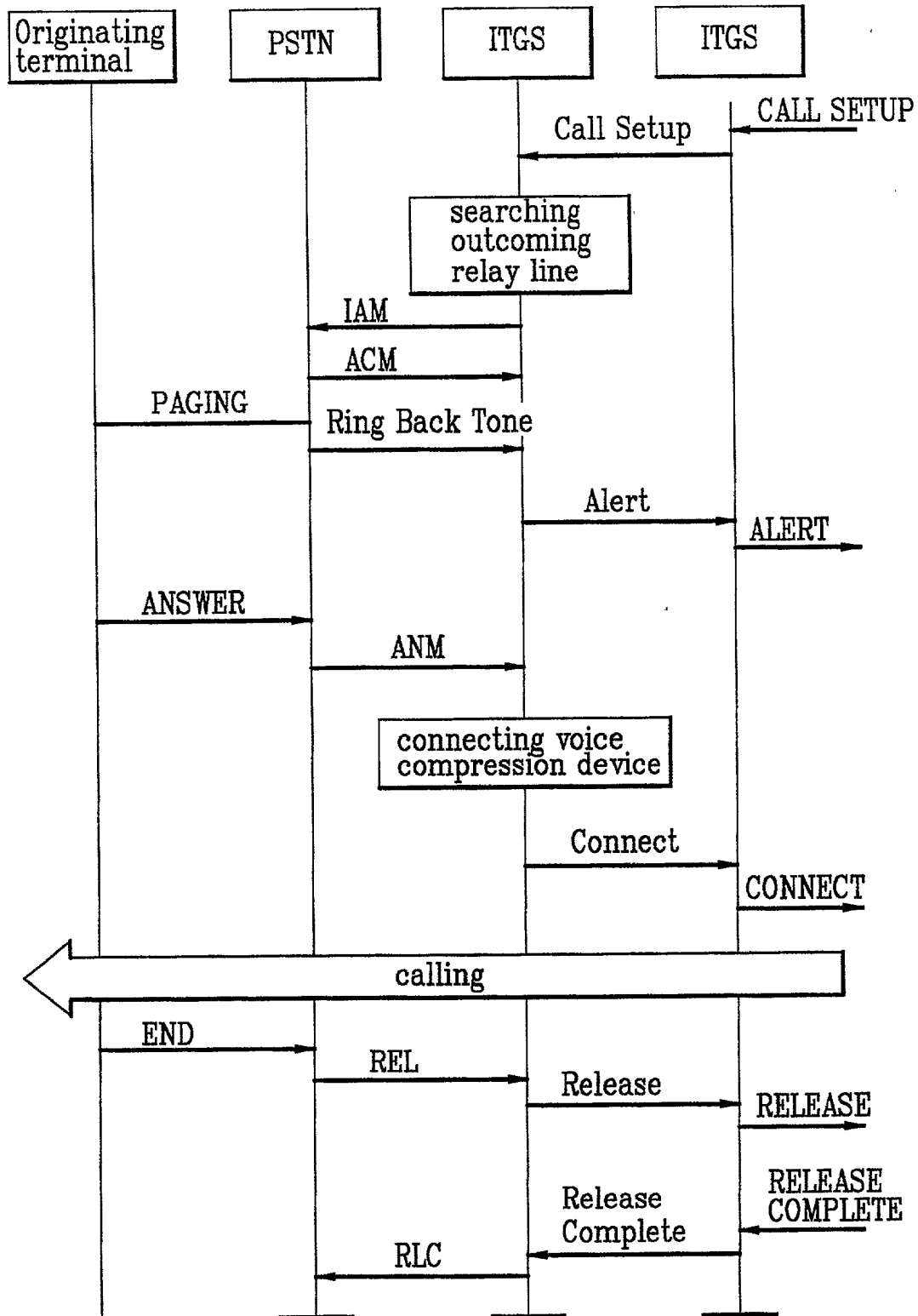
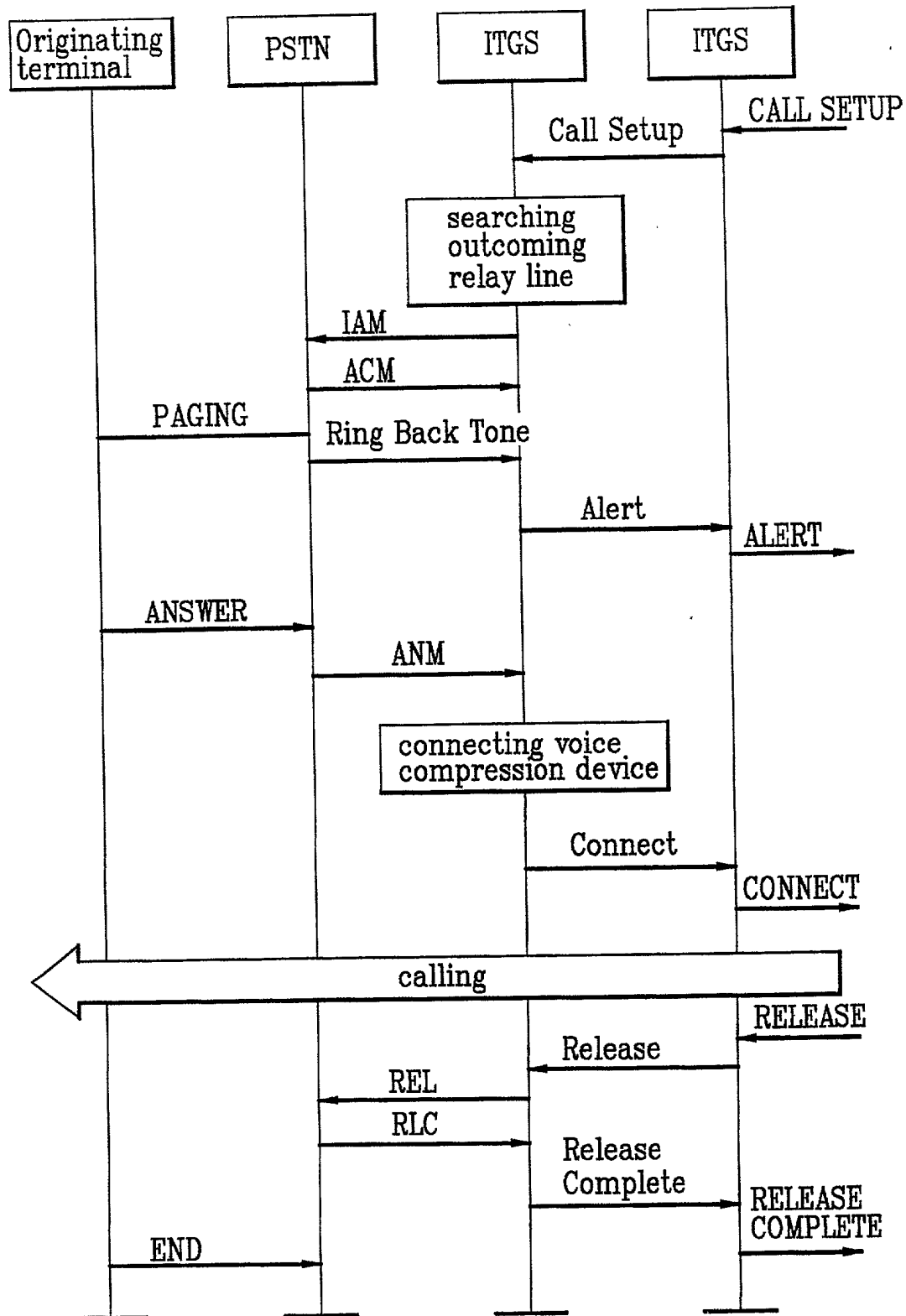


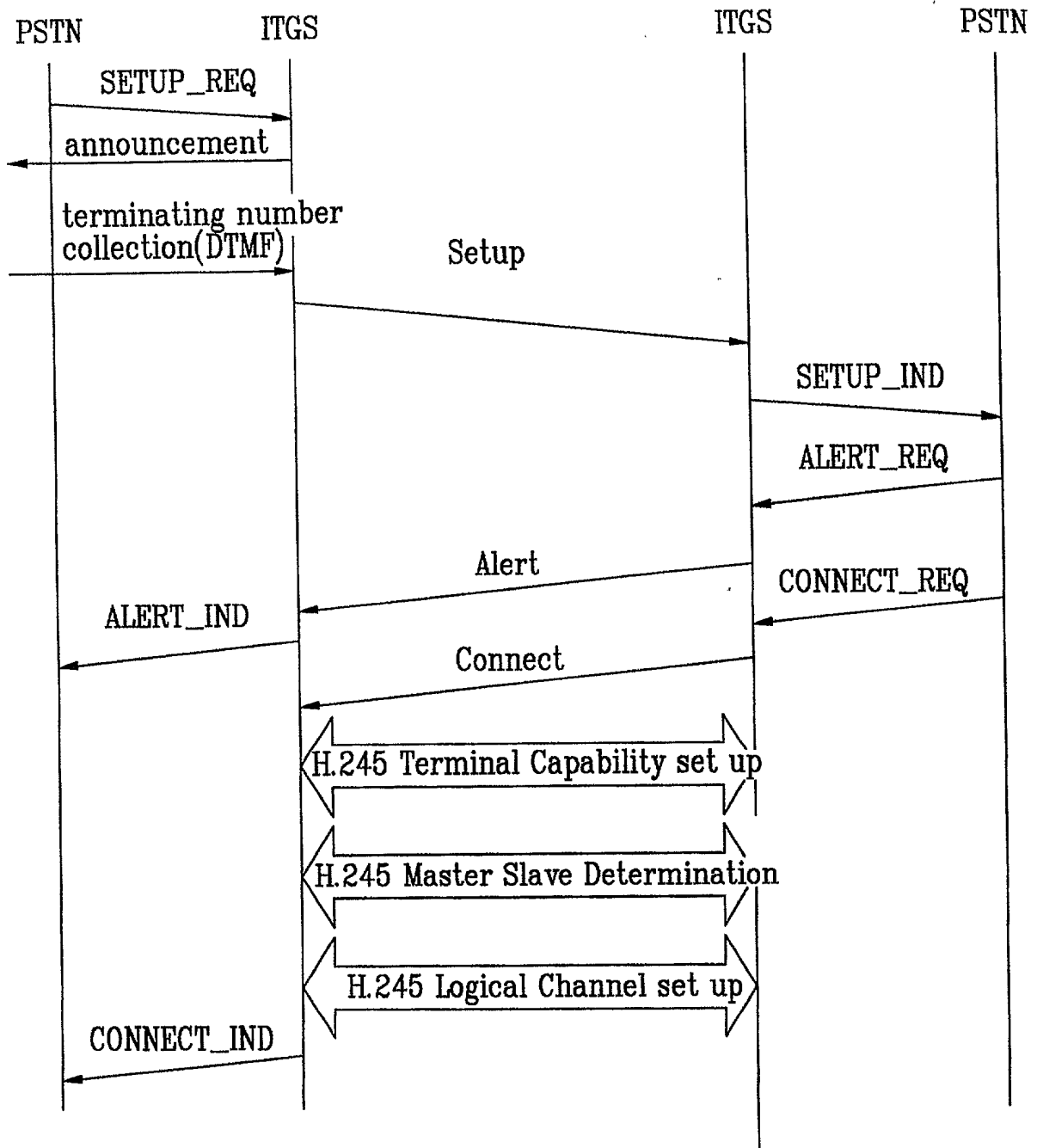
FIG. 9

FIG. 10



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FIG. 11



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FIG. 12

